APPLICATION		REVISION			
NEXT ASSY.	USED ON	LTR	DESCRIPTION	DATE	APPROVED
	C65	A	**PRELIMINARY RELEASE**	1-13-91	J. PORTER
		B	REVISED PER ECO 910193	6-26-91	R. Bucks

1.0 DESCRIPTION

This specification describes the features and part number for a Video Interface Chip (VIC). This device shall include the following features:

a. newvic mode
b. 80 column character and 640 horizontal pixel mode
c. Scan interlace and 400 line mode
d. character attributes (blink, hilite, underline, reverse)
e. fast clock mode (3.58 vs 1.02 mhz)
f. bitplane mode
g. color palettes
h. additional ROM
i. 1280H pixel mode
j. display address translator (DAT)
k. horizontal and vertical positioning
l. external sync
m. alternate character set
n. chroma killer

1.1 CONFIGURATION

This device shall be configured in a standard 84-pin PLCC package.

1.2 SOURCES

Refer to Approved Vendor List for sources.

1.3 APPLICABLE DOCUMENTS

Commodore Engineering Policy 1.02.007 Integrated Circuit Qualification Procedure
Commodore Engineering Policy 1.02.008 Integrated Circuit Process Test Specification

COMMODORE P. N.	STATUS				
390489-01	INACTIVE				
390489-02	INACTIVE				
390489-03	ACTIVE				

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES.
TOLERANCES:
ANGLES +/- 1 DEGREE
2 PLACE DECIMALS +/- 0.02
3 PLACE DECIMALS +/- 0.010

DRAWN Mike Rivers	DATE
SYSTEM ENG.	DATE
TEST ENG	DATE
COMP. ENG Drew Shannon	DATE
CIRCUIT ENG.	DATE

Commodore

1200 WILSON DRIVE
WEST CHESTER, PA. 19380
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TITLE: IC, LSI, VICIII, C4567

SIZE	DRAWING NUMBER
B	390489
SCALE	SHEET 1 OF 3

C4567R4 PINOUTS

pin number	pin name	active state	type	direction	function
1	IO2*	low		output	user I/O enable
2	IO1*	low		output	user I/O enable
3	NOIO			output	CPU I/O device disable
4	AEC*			output	CPU address/data/rw enable
5	RW*			I/O	CPU read/write
6	NOMAP			input	CPU physical/virtual address
7	EXROM*	low	pullup	input	cartridge select
8	GAME*	low	pullup	input	cartridge select
9	EXPAND*	low	pullup	input	VIC chip deselect
10	SIDCLK			output	SID chip clock
11	VCC		supply		
12	CAS*	low		output	generic column address strobe
13	CASB*	low		output	column address strobe bank A
14	CASA*	low		output	column address strobe bank B
15	RAS*	low		output	generic row address strobe
16	CPUCLK			output	CPU clock source
17	DOTCLK			output	7 Mhz to timers
18	XTAL1			input	14 Mhz clock input
19	XTAL2			input	Chroma clock input
20	MA0			output	-
21	MA1			output	.
22	MA2			output	. multiplexed DRAM address
23	MA3			output	. buss for bank A and part
24	MA4			output	. bank B
25	MA5			output	.
26	MA6			output	.
27	MA7			output	-
28	MB7			output	-
29	MB6			output	. exceptions for bank B
30	MB5			output	-
31	A16			I/O	-
32	A15			I/O	.
33	A14			I/O	.
34	A13			I/O	.
35	A12			I/O	.
36	A11			I/O	.
37	A10			I/O	. unmultiplexed CPU address
38	A9			I/O	. bus
39	A8			I/O	.
40	A7			I/O	.
41	A6			I/O	.
42	A5			I/O	.
43	A4			I/O	.
44	A3			I/O	.
45	A2			I/O	.

pin number	pin name	active state	type	direction	function
46	A1			I/O	.
47	A0			I/O	-
48	EXTV*	low	pullup	input	external vertical sync
49	EXTH*	low	pullup	input	external horizontal sync
50	LP*	low	pullup	input	light pen
51	RES*	low		input	system reset
52	IRQ*	low	od	output	interrupt request
53	VSS		supply		
54	D7			I/O	-
55	E7			I/O	.
56	D6			I/O	.
57	E6			I/O	.
58	D5			I/O	.
59	E5			I/O	.
60	D4			I/O	.
61	E4			I/O	. CPU and video
62	D3			I/O	. data busses
63	E3			I/O	.
64	D2			I/O	.
65	E2			I/O	.
66	D1			I/O	.
67	E1			I/O	.
68	D0			I/O	.
69	E0			I/O	-
70	VCC		supply		
71	PSYNC			input	CPU sync
72	MEMCLK			output	memory clock
73	DISK*	low		output	disk chip enable
74	CHROMA		analog	output	NTSC chrominance signal
75	RVIDEO		analog	output	red video
76	GVIDEO		analog	output	green video
77	BVIDEO		analog	output	blue video
78	LUMA		analog	output	monochrome video
79	SYNC			output	digital sync
80	FGBG			output	digital video
81	SID*	low		output	SID chip select
82	ROML*	low		output	cartridge ROM enable low region
83	ROMH*	low		output	cartridge ROM enable high region
84	ROM*	low		output	system ROM enable

APPROVED VENDOR LIST

This sheet must be removed from this document before the document is shown or transmitted to a vendor.

Commodore Part Number	Vendor	Vendor Part Number
390489-01	CSG	C4567 R5 P
390489-02	CSG	C4567 R6 P
390489-03	CSG	C4567 R7 P

Commodore			TITLE: IC, LSI, VICIII, C4567	
SIZE	DRAWING NUMBER: 390489	REV.: B	SCALE	SHEET i OF i